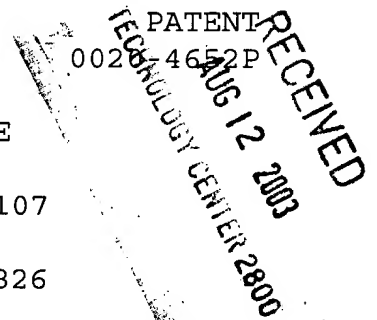




MS AF  
REPLY UNDER  
37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2826



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Narihiro MOROSAWA et al. Conf.: 7107  
Appl. No.: 09/471,173 Group: 2826  
Filed: December 23, 1999 Examiner: QUINTO, Kevin  
For: INSULATED GATE TRANSISTOR AND PROCESS  
FOR FABRICATING THE SAME

REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

MS AF

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 8, 2003

Sir:

In reply to the Office Action dated May 21, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes List of claims and Remarks.